

## Call for Papers

### 1<sup>st</sup> International Career Outreach Conference “Trends in Sustainable Design, Technology and Innovation” Organized by the Microsystem Design-Integration Lab

**Bidhan Chandra College, Asansol, WB, India**  
**26<sup>th</sup> -28<sup>th</sup> November 2022, 10:00 AM-4:00 P.M IST**

A series of career outreach workshops, by Microsystem Design-Integration Lab, Dept. of Physics, Bidhan Chandra College, Asansol, West Bengal, India, was organized in last two years, intended to help students thrive in a world of constant change, providing exposure, career-readiness and internship opportunities and professional career advisories at domestic and international level. Bidhan Chandra College, Asansol, West Bengal, is proud to enable Advanced Research, Design, Computational Facilities, and Google Workspace for Education, for its all faculties, staffs and above all students, during this difficult time of pandemic, facilitating the teaching and learning with cloud based collaborative tools. We are pleased to take a leap further by inviting the students, scholars, and faculties for two-day International Career Outreach Conference on “Trends in Sustainable Design, Technology and Innovation”, technically sponsored by Confederation of Education Excellence (proposed), in blended mode.

### Esteemed Keynote Speakers



*Prof. Mrinal Kanti Musib,  
Sr. Lecturer,  
Dept. of Biomedical Engineering,  
National University of Singapore,  
Singapore*



*Prof. Sourangshu Mukhopadhyay,  
Professor,  
Dept. of Physics,  
The University of Burdwan,  
India*



*Prof. Chandan Kumar Sarkar,  
1851 Royal Commission Fellow;  
IEEE Electron Device Society  
Distinguished Lecturer;  
Formerly Professor,  
Dept. of Electronics and Telecom.,  
Jadavpur University, India*



*Prof. Hiroshi Inokawa,  
Professor,  
Research Institute of Electronics,  
Shizuoka University,  
National University Corporation,  
Japan  
(Virtual Talk)*



*Prof. Tapas Sen,  
FRSC, FHEA,  
Reader,  
School of Natural Sciences,  
Central Lancashire university,  
UK  
(Virtual Talk)*



*Prof. Wong Kin Shun, Terence,  
Professor,  
School of Electrical & Electronic  
Engineering, Nanyang  
Technological University,  
Singapore  
(Virtual Talk)*

## Scope

Technical papers, under the following broad subject areas describing original, previously unpublished research related to Sustainable Design, Technology and Innovation, not currently under review by another conference or journal, are solicited. The specific topics of the conference include, but are not limited to:

**Track I: Applied and Natural Sciences:** Physical, Chemical, Mathematical, Biological and Material Sciences.

**Track II: Design and Engineering:** Concepts in Electronics, Electrical, Biomedical, Computational Engineering, e.g. Microelectronics and Devices, Optoelectronics and photonics, Thin Film Electronics, Material and Process Engineering, Emerging Memories, Interconnect-3D Integrations-MEMS, Power-High-speed Devices and Materials, Quantum information Systems and Devices, Organic-Molecular-Bio-electronics, Hybrid and Renewable Energy Sources, Photovoltaics-Energy Harvesting-Battery-related Technology, Biomaterials-Biomedicine, Medical Imaging, Biomechanics, Computational Bioengineering, Integrated Microfluidics. Cyber-physical Systems, Communication Systems, Signal Analysis, Data Science, Artificial Intelligence and Machine Learning, Optical and Communication Systems, Cloud and Mobile Computing, Deep Learning, Networks & Securities, Intelligent Systems and Robotics, Internet of Things.

## Publication

For timely publication, completed texts of abstract and manuscripts must be prepared according guidelines below.

**Abstract Submission Deadline:** 15<sup>th</sup> Nov, 2022

**Submission Email:** [seminar@bccollegeasansol.ac.in](mailto:seminar@bccollegeasansol.ac.in)

**Subject of Email:** Abstract\_Second Name\_Track

**Abstract Format:** English, Open format, 200 words, 12 point, Single Spacing

**Acceptance Notification:** 20<sup>th</sup> Nov, 2022

**Camera Ready Submission:** Dec 15<sup>th</sup>, 2022

**Proceeding Format:** English, Open format, Max 6 Pages, 12 point, Single Spacing  
(to be submitted with plagiarism report <10%)

**All accepted papers will be e-published in: Conference Proceedings (Edited Volume with ISBN).**

## Copyrights

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## Registration

**Online Registration:** 1<sup>st</sup> Nov to 21<sup>st</sup> Nov, 2022

**Online Registration Link:** <https://forms.gle/kTTV5JhiCrpoFtwY8>

**Payment details may be submitted to Email:** [seminar@bccollegeasansol.ac.in](mailto:seminar@bccollegeasansol.ac.in)

**Subject of Email:** Registration\_Second Name\_Track

**Confirmation e-mail for registration and participation will be sent after payment confirmation.**

## Registration Fees:

Participants type	Participant On-site: Day I Virtual: Day II	Participant Virtual: Day II	Participant On-site: Day I Virtual: Day II	Participant Virtual: Day II
	With Paper			
Faculty /Academics/Industry	700 INR	300 INR	300 INR	Free
Research Scholars	500 INR	300 INR	300 INR	Free
UG Students/PG students	Free	Free	Free	Free

## Payment Details:

**Bank Name :** SBI  
**IFSC Code :** SBIN0000011  
**Beneficiary Name:** BIDHAN CHANDRA COLLEGE, ASANSOL

**Branch Name:** Asansol, Burdwan  
**Account Number :** 11214097180

**Refund Policy:** Conference Fee once paid would not be refunded under any circumstance.

## Organizing Committee

**Patron:** Prof. Falguni Mukhopadhyay, Hon'ble Principal, Bidhan Chandra College

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**Dr. Basudev Pradhan**, Dept. of Energy Engineering, Central University of Jharkhand, Ranchi, India;

Associate Editor, Applied Physics A, Springer Journal; Guest Associate Editor, Frontiers in Materials.

**Organized by** Microsystem Design-Integration Lab, Dept. of Physics, Bidhan Chandra College,

**Partially supported by the** Device Development Programme (DDP),

**Department of Science & Technology (DST), Government of India.**

**For further information on Collaboration, Scope of Research and Internship**

**Website:** [www.bccollegeasansol.ac.in](http://www.bccollegeasansol.ac.in) || **Email:** [amit@bccollegeasansol.ac.in](mailto:amit@bccollegeasansol.ac.in)

### Active Research Project Currently:

Competitive Research Grant under Device Development Programme,  
by the Department of Science & Technology (DST), Ministry of Science and Technology, Government of India.

### Microsystem Design-Integration Lab Resources and Design Capabilities:

COMSOL Multiphysics with following packages: Wave Optics Module, Ray Optics Module, RF Module, AC/DC Module, Semiconductors Module, MEMS Module, Heat Transfer Module, Design Module, Material Library module;

NI Multisim Software; NI Ultiboard Software; NI LabVIEW Software and DAQs;

Siemens Solid Edge Software for 3D Design, Simulation;

Advanced Design and Computation Facility;

### Active collaboration:

Advanced Device Research Division, Research Institute of Electronics, Shizuoka University,

National University Corporation, Hamamatsu, Japan

Dept. of Electrical and Computer Engineering, National University of Singapore

Organic Electronics Research Center (OERC), Ming Chi University of Technology, Taipei, Taiwan

## Career Outreach Conference Supported by



Technical expertise in PV, Energy storage and Hydrogen  
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